

NAK1-BG43

IN THE U.S. PATENT AND TRADEMARK OFFICE

In Re Application of:

Taketo Heishi et al.

Serial No.: 09/280,777

Filed: March 29, 1999

For: INSTRUCTION CONVERTING APPARATUS
THAT GENERATES PARALLEL EXECUTION
CODE FROM A PLURALITY OF
INSTRUCTIONS, PROCESSOR FOR
EXECUTING THE GENERATED PROGRAM
STORAGE MEDIUM STORING PARALLEL
EXECUTION CODE GENERATED FROM A
PLURALITY OF INSTRUCTIONS ...



Examiner:

Group Art Unit:

Irvine, CA 92614

June 21, 1999

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In an attempt to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97 and 1.98, applicants wish to bring to the attention of the U.S. Patent Office the following references:

Japanese Laid-Open Patent Application
No. 4-282726 (with English abstract)

Japanese Laid-Open Patent Application
No. 7-175652 (with English abstract)

Copies of these references and a form PTO 1449 are attached.

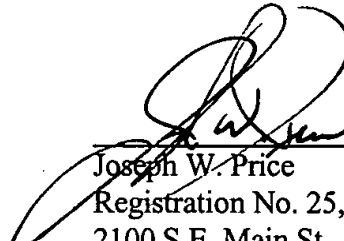
If the Examiner believes that a telephone conference would help further the prosecution of this case, he is respectfully requested to contact the undersigned attorney at the listed telephone number.

Respectfully submitted,

PRICE, GESS & UBELL

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington DC 20231

on June 22, 1999
Karen L. Miyakawa
Karen L. Miyakawa
Signature
June 22, 1999
Date



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